

INFORMATION DISCLOSURE STATEMENT PURSUANT TO
37 C.F.R. §§1.97-1.99

PATENT APPLICATION

Applicant(s): Ming-i M. Lin et al.

Docket No.: END920030080US1

FOR: MULTILEVEL PARALLEL CRC GENERATION AND CHECKING CIRCUIT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and pursuant to 37 C.F.R. §§1.97-1.99, Applicant(s) hereby notifies the U.S. Patent and Trademark Office of the documents listed on the attached Form PTO-1449. Applicant respectfully submits that all pending claims are patentable over the foregoing references, alone or in combination. The Examiner is requested to initial the enclosed Form PTO-1449 and return a copy thereof to the undersigned.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant reserves the right to dispute any of the listed documents as prior art during examination. Further, Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. Furthermore, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist. This Information Disclosure Statement is being filed within three months of the filing date of the captioned patent application, and therefore no certification under 37 C.F.R. §1.97(e) or fee under 37 C.F.R. §1.17(p) is required.

Respectfully submitted,

Dated: 12/04/2007

By Jack P. Friedman
Jack P. Friedman
Reg. No. 44,688

Enclosures: PTO-1449

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

ATTY DOCKET NO.
END920030080US1

SERIAL NO.

Ming-i M. Lin et al.

FILING

GROUP

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,560,746	5/2003	Morsberger			
	6,223,320	4/2001	Dubey et al.			
	5,878,057	3/1999	Maa			
	5,787,094	7/1998	Cecchi et al.			
	5,748,652	5/1998	Kim			
	5,671,238	9/1997	Chen et al.			
	5,383,204	1/1995	Gibbs et al.			
	5,375,127	12/1994	Leak et al.			
	5,282,214	1/1994	Dravida			
	5,132,975	7/1992	Avaneas			
	4,937,828	6/1990	Shih et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Albertengo et al., PARALLEL CRC GENERATION, 10/1990 IEEE, pages 63-71.
		Pei et al., HIGH-SPEED PARALLEL CRC CIRCUITS IN VLSI, Vol. 40, No. 4, April 1992 IEEE, pages 653-656.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,454,600	6/1984	LeGresley			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			McClannahan et al., HIGH-SPEED PARALLEL CYCLIC REDUNDANCY CHECK GENERATOR, Vol. 33, No. 5, October 1990 IBM Technical Disclosure Bulletin.

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